

210067US-387-39-2



8/A  
6/12/02  
D. Bell

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :  
HIDEMASA ZAMA ET AL : EXAMINER: TAN, V.  
SERIAL NO: 09/883,959 :  
FILED: JUNE 20, 2001 : GROUP ART UNIT: 2819  
FOR: SEMICONDUCTOR INTEGRATED :  
CIRCUIT, LOGIC OPERATION  
CIRCUIT, AND FLIP FLOP

AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

RECEIVED  
JUN 10 2002  
TC 2800 MAIL ROOM

SIR:

In response to the Official Action dated March 7, 2002, please amend the above-identified application as follows:

IN THE CLAIMS

1. (Amended) A semiconductor integrated circuit comprising:

06/12/2002 010000005 150030 09/83959

01 FC: 252. (a plurality of gate circuits; and

a control circuit configured to control the operation of some gate circuits among said plurality of gate circuits,

each of said some gate circuits among said plurality of gate circuits including:

a logic circuit constituted by a plurality of first transistors; and

a switch circuit which includes second and third transistors controlled to turn on/off by said control circuit, each having a threshold voltage higher than that of each of said first

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